

PANEL #0

TERMINAL BOARD TBA +5V

PIN	SIGNAL	VIA	LEFT SIDE	RIGHT SIDE
1	+5V	-	LOGIC SUPPLY	LOGIC SUPPLY
2	+5V	-	-	EL-1230
3	+5V	J10	PIN-2, 4	PANELS 7 & 9
4	+5V	J10	PIN-6, 8	-
5	+5V	J10	PIN-10, 12	-
6	+5V	J10	PIN-14, 16	-
7	+5V	J10	PIN-18,20	-
8	+5V	J10	PIN-22, 24	-
9	+5V	J10	PIN-26	-
10	+5V	-	-	PANEL 9, PIN 2

PINS 1-10 JUMPERED ON TOP

TERMINAL BOARD TBB GND

PIN	SIGNAL	VIA	LEFT SIDE	RIGHT SIDE
1	GND	J10	GALIL LSCOM	PIN-27,28
2	GND	J10	PIN-21	-
3	GND	J10	PIN-23, 25	-
4	GND	J10	PIN-17, 19	INTERLOCKS
5	GND	J10	PIN-13, 15	INTERLOCKS
6	GND	J10	PIN-9, 11	-
7	GND	J10	PIN-5, 7	-
8	GND	J10	PIN-1, 3	PANELS #7, #9
9	GND	-	-	EL-1230
10	GND	-	LOGIC SUPPLY	LOGIC SUPPLY

PINS 1-10 JUMPERED ON TOP

TERMINAL BOARD TBE AC

PIN	SIGNAL	LEFT SIDE	RIGHT SIDE
1	AC-H	LOGIC	-
2	AC-H	OPTO-22 PANEL-7	LOGIC SUPPLY
3	AC-H	+48V SUPPLY	AC LINE H

4	AC-L	+48V SUPPLY	AC LINE L, MOTION WARNING, MOVEMENT ALARM
5	AC-L	MOVEMENT & MOTION AC	LOGIC SUPPLY
6	AC-L	LOGIC	FANS 1 AND 2

PINS 1-3 JUMPERED

PINS 4-6 JUMPERED

UNIVERSITY OF CALIFORNIA
LICK OBSERVATORY

POWER DISTRIBUTION TERMINAL BOARDS
CRADLE ENCLOSURE
DEIMOS SPECTROGRAPH

REVISION

04-26-00 CORRECTED WIRE FROM TBB-11 FROM J13-5 TO J13-5
 08-07-00 ADDED WIRING FOR RELAY PANEL 8
 01-17-01 REMOVED ROTATION STAGE (NOW ON LUNIX PC)
 03-21-01 ADDED 'MOVEMENT_WARNING' AND 'MOTION_ALARM' AC CONNECTIONS
 11-19-01 CORRECTED LEFT SIDE OF PIN 4 OF TBB: WAS 11, 19 - IS 17, 19

DES'N BY: B. Alcott

ORIGIN DATE: 02-27-96

DWG. NO.

NUM. 1 OF 1

DRAWN BY:

MODIFY DATE: 11-19-01

PATH: DEIMOS\VTBAG

REV. A

EL-3119