

MAIN (CABLE J1, TB-1)

PIN	FUNCTION	I/O	ASSIGNMENT	PIN	FUNCTION	I/O	ASSIGNMENT	PIN	FUNCTION	I/O	ASSIGNMENT
1	+AAX	I	-	36	+VCC	0	-	75	AN1	I	JT1-5 ANALOG INPUT CHANNEL A
2	-AAX	I	-	37	AMPENW	0	-	76	AN2	I	JT1-6 ANALOG INPUT CHANNEL C
3	+ABX	I	-	38	AMPENZ	0	-	77	AN3	I	JR1-1 RENISHAW 1 SETUP
4	-ABX	I	-	39	AMPENY	0	-	78	AN4	I	JR2-1 RENISHAW 2 SETUP
5	+AAY	I	-	40	AMPENX	0	-	79	AN5	I	(**NOT USED)
6	-AAY	I	-	41	LSCOM	I	J10-14 EXTERNAL +5V	80	AN6	I	(**NOT USED)
7	+ABY	I	-	42	HOMEW	I	-	81	AN7	I	(**NOT USED)
8	-ABY	I	-	43	RLSW	I	-	82	AN8	I	(**NOT USED)
9	+AAZ	I	-	44	FLSW	I	-	83	+MAX	I	J1-7 ROTATION ENCODER +A
10	-AAZ	I	-	45	HOMEZ	I	-	74	-MAX	I	J1-8 ROTATION ENCODER -A
11	+ABZ	I	-	46	RLSZ	I	-	85	+MBX	I	J1-9 ROTATION ENCODER +B
12	-ABZ	I	-	47	FLSZ	I	-	86	-MBX	I	J1-10 ROTATION ENCODER -B
13	+AAW	I	-	48	HOMEY	I	-	87	+INX	I	J21-3 SECONDARY ENCODER +I
14	-AAW	I	-	49	RLSY	I	-	88	-INX	I	J21-2 SECONDARY ENCODER -I
15	+ABW	I	-	50	FLSY	I	-	89	ANALOG GND*	I	JM1-11, JT1-8 ANALOG GND
16	-ABW	I	-	51	HOMEX	I	J1-19 X-AXIS HOME	90	+VCC	I	-
17	GND	I	-	52	RLSX	I	J1-16 X-AXIS REV LIMIT	91	+MAY	I	JR1-14 RENISHAW 1 +A
18	+VCC	I	-	53	FLSX	I	J1-15 X-AXIS FWD LIMIT	92	-MAY	I	JR1-6 RENISHAW 1 -A
19	OUTCOM	0	J10-12 EXTERNAL +5V	54	+VCC	-	-	93	+MBY	I	JR1-13 RENISHAW 1 +B
20	ERROR	0	-	55	GND	-	JM1-2, JM1-6	94	-MBY	I	JR1-5 RENISHAW 1 -B
21	RESET	I	-	56	INCOM	I	J10-8 EXTERNAL +5V	95	+INY	I	JR1-11 RENISHAW 1 +INDEX
							-	96	-INY	I	JR1-4 RENISHAW 1 -INDEX
22	CMP	0	-	57	XLATCH	I	ROTATION SECONDARY LIMIT	97	+MAZ	I	JR2-14 RENISHAW 2 +A
23	MOCMDW	0	-					98	-MAZ	I	JR2-6 RENISHAW 2 -A
24	SIGNW	0	-	58	YLATCH	I	-	99	+MBZ	I	JR2-13 RENISHAW 2 +B
				59	ZLATCH	I	-	100	-MBZ	I	JR2-5 RENISHAW 2 -B
25	PWMW	0	-					101	+INZ	I	JR2-11 RENISHAW 2 +INDEX
26	MOCMDZ	0	-	60	WLATCH	I	-	102	-INZ	I	JR2-4 RENISHAW 2 -INDEX
27	SIGNZ	0	-					103	GND	I	JM1-14 GND
								104	+VCC	I	-
				61	IN5	I	JM1-1 OVER CENTER	105	+MAW	I	J21-7 SECONDARY ENCODER +A
				62	IN6	I	JR1-10 RENISHAW 1 LIMIT SW	106	-MAW	I	J21-6 SECONDARY ENCODER -A
28	PWMZ	0	-	63	IN7	I	JR2-10 RENISHAW 2 LIMIT SW	107	+MBW	I	J21-5 SECONDARY ENCODER +B
29	MOCMDY	0	-					108	-MBW	I	J21-4 SECONDARY ENCODER -B
30	SIGNY	0	-	64	IN8	I	JM1-10 INPUT BIT 8	109	+INW	I	-
				65	ABORT	I	-	110	-INW	I	-
				66	OUT1	0	J1-21 ROTATION FIDUCIAL ENABLE	111	+12V	I	-
31	PWMY	0	-	67	OUT2	0	-	112	-12V	I	-
32	MOCMDX	0	-	68	OUT3	0	JM1-12 OUTPUT BIT 3				
33	SIGNX	0	-	69	OUT4	0	JM1-13 OUTPUT BIT 4				
				70	OUT5	0	JT1-1 ANALOG CONTROL BIT 0				
				71	OUT6	0	JT1-2 ANALOG CONTROL BIT 1				
34	PWMX	0	-	72	OUT7	0	JT1-3 ANALOG CONTROL BIT 2				
35	ISO*	0	J10-11 EXTERNAL GND	73	OUT8	0	JM1-7 OUTPUT BIT 8				
				74	GND	0	-				

J1 = X-AXIS CONNECTOR
 J21 = X-AXIS SECONDARY ENCODER CONNECTOR
 J10 = LOGIC SUPPLY CONNECTOR
 JR1 = RENISHAW CONNECTOR 1
 JR2 = RENISHAW CONNECTOR 2
 JM1 = MISCELLANEOUS I/O CONNECTOR
 JT1 = TEMPERATURE INPUT CONNECTOR

**NOT AVAILABLE IN SECONDARY FIFO

ALSO SEE EL-3110

UNIVERSITY OF CALIFORNIA
 LICK OBSERVATORY

SIGNAL ASSIGNMENTS
 CRADLE AMPLIFIER
 DEIMOS SPECTROGRAPH

REVISION

01-17-01	REMOVED ROTATION CONTROL SIGNALS (NOW ON LINUX PC)
02-16-01	ADDED SIGNAL DESIGNATIONS
07-10-01	MOVED RENISHAW ANALOG INPUTS AND CORRECTED VARIOUS LABELS
07-11-01	ADDED NOTE ABOUT AN5-8 NOT AVAILABLE
07-30-01	CORRECTED NOTE ABOUT AN5-8 NOT AVAILABLE
11-19-01	CORRECTED LABELS FOR AN5-8 TO NOT USED
08-13-02	REMOVED OLD LABELS FROM Y, Z, AND WLATCH INPUTS

DES'N BY: B. Alcott

ORIGIN DATE: 01-17-01

DWG. NO. NUM. 1 OF 1

DRAWN BY:

MODIFY DATE: 08-13-02

PATH: DEIMOS\SIGNALSE

REV. A

EL-3111