

MAIN (JD2)

PIN	FUNCTION	ASSIGNMENT	PIN	FUNCTION	ASSIGNMENT	
1	GND		31	MOT CMD H	} SPARE SIGNALS TO SERVO AMPS	
2	+5V		32	AMP EN H		
3	NC		33	A+ E	} SLITMASK	
4	NC		34	A- E		
5	LIM COMMON	} LIMIT SWITCH SIGNALS	35	B+ E		
6	FWD LIM E		} SPARE	36		B- E
7	REV LIM E			} SPARE		37
8	HOME E		} ENCODERS		38	I- E
9	FWD LIM F				} SPARE	39
10	REV LIM F	} SPARE		40		A- F
11	HOME F			} SPARE	41	B+ F
12	FWD LIM G	} SPARE	42		B- F	
13	REV LIM G		} SPARE	43	I+ F	
14	HOME G	} SPARE		44	I- F	
15	FWD LIM H		} SPARE	45	A+ G	
16	REV LIM H	} SPARE		46	A- G	
17	HOME H		} SPARE	47	B+ G	
18	OUTPUT 9	} SPARE		48	B- G	
19	INPUT COM.		} SPARE	49	I+ G	
20	LATCH E	} SPARE		50	I- G	
21	LATCH F		} SPARE	51	A+ H	
22	LATCH G	} SPARE		52	A- H	
23	LATCH H		} SPARE	53	B+ H	
24	INPUT 24	} SPARE		54	B- H	
25	MOT CMD E		} SPARE	55	I+ H	
26	AMP EN E	} SPARE		56	I- H	
27	MOT CMD F		} SPARE	57	+12V	
28	AMP EN F	} SPARE		58	-12V	
29	MOT CMD G		} SPARE	59	+5V	
30	AMP EN G	} SPARE		60	GND	

GENERAL I/O (JD5)

PIN	FUNCTION	ASSIGNMENT
1	INPUT 17	
2	INPUT 18	
3	INPUT 19	
4	INPUT 20	
5	INPUT 21	
6	INPUT 22	
7	INPUT 23	
8	GND	
9	+5V	
10	OUTPUT 9	SLITMASK
11	OUTPUT 10	SPARE
12	OUTPUT 11	SPARE
13	OUTPUT 12	SPARE
14	OUTPUT 13	-
15	OUTPUT 14	-
16	OUTPUT 15	-
17	OUTPUT 16	-
18	INPUT 16	-
19	INPUT 15	-
20	INPUT 14	-
21	INPUT 13	-
22	INPUT 12	SPARE
23	INPUT 11	SPARE
24	INPUT 10	SPARE
25	INPUT 9	SLITMASK
26	INPUT COM.	

AUXILIARY ENCODER (JD3)

PIN	FUNCTION	ASSIGNMENT
1	SAMPLE CLK	
2	RESERVED	
3	B- H	
4	B+ H	
5	A- H	
6	A+ H	
7	B- G	
8	B+ G	
9	A- G	
10	A+ G	
11	B- F	
12	B+ F	
13	A- F	
14	A+ F	
15	B- E	
16	B+ E	
17	A- E	
18	A+ E	
19	+5V	
20	GND	

NOTE: THOUGH NOTED AS X, Y, Z, AND W-AXIS
WE HAVE DESIGNATED THESE AS E, F, G,
AND H-AXIS

ALSO SEE EL-3029, 3067

REVISION

06-19-97 SWAPPED SEVERAL STAGES
03-13-00 MOVED FIDUCIAL AND SECONDARY LIMITS TO CORRECT PINS
06-30-00 ADDED NOTE ABOUT AXIS DESIGNATIONS AND REMOVED OLD REFERENCES
06-14-02 TO THE FILTER WHEEL
CHANGED LABELS FROM SLIT CASSETTE TO SLITMASK

UNIVERSITY OF CALIFORNIA
LICK OBSERVATORY

SIGNAL ASSIGNMENTS
SECOND BANK OF CONNECTORS CONTROLLER #2
DEIMOS SPECTROGRAPH

DES'N BY: B. Alcott

ORIGIN DATE: 04-05-95

DWG. NO.

NUM. 1 OF 1

Last SN:

MODIFY DATE: 06-14-02

PATH: DEIMOS\SIGNALS4

REV. A

EL-3023