



UNIVERSITY OF CALIFORNIA
 LICK OBSERVATORY

INTERCONNECT CARD JUMPERS
 HI RHO CCD CONTROLLER

REVISION

- 10-01-00 ADDED JUMPER AT SPARE6-1
- 06-19-01 CHANGED JUMPERS TO CONNECT H2AL TO PIN A15 AND H3AL TO PIN C19
- 07-10-01 ADDED HSYNC FROM 0-19 TO 3-17 OUTPUT POST AND VSYNC FROM 1-19 TO 4-17 OUTPUT POST
- 07-10-01 ADDED WIRE FROM DCR/SPARE17 PIN TO VDD-0 OUTPUT PIN. THIS ADDS N+ SUB VOLTAGE FROM BOARD0 PIN17 TO OUTPUT PIN A1.
- 08-07-01 CHANGES JUMPER FROM DCR0 TO VDD0. NOW: Vpog1 GOES TO VDD0 OUT

DES'N BY: B. Alcott

ORIGIN DATE: 03-06-00

DWG. NO. NUM. 1 OF 1

DRAWN BY:

MODIFY DATE: 08-07-01

PATH: HI_RHO_CCD\HI_RHO_JUMPER

REV. A

EL-1280