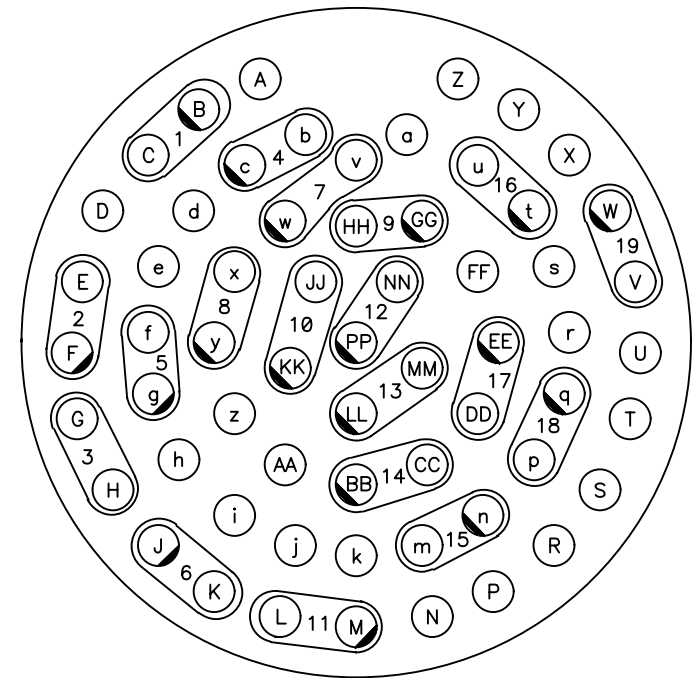


# J1 WIRING

PIN	SIGNAL	PIN	SIGNAL	PIN	SIGNAL
A	SHIELD	X	NOT USED	t	GND
B	GND	Y	NOT USED	u	Vrg
C	N+ SUB V	Z	NOT USED	v	H3AR
D	NOT USED	a	NOT USED	w	GND
E	DEWAR ID 0	b	Vsub	x	H3AL
F	DEWAR ID 1	c	GND	y	GND
G	DEWAR ID 2	d	NOT USED	z	NOT USED
H	DEWAR ID 3	e	NOT USED	AA	NOT USED
J	DEWAR ID 4	f	H1AL	BB	GND
K	NOT USED	g	GND	CC	SW (AL)
L	Vr (AL)	h	NOT USED	DD	V1
M	GND	i	NOT USED	EE	GND
N	NOT USED	j	NOT USED	FF	NOT USED
P	NOT USED	k	NOT USED	GG	GND
R	NOT USED	m	H2AR	HH	V2
S	NOT USED	n	GND	JJ	TG A
T	NOT USED	p	H2AL	KK	GND
U	NOT USED	q	GND	LL	GND
V	Vopg (AL)	r	NOT USED	MM	V3
W	GND	s	NOT USED	NN	N+ GUARD
				PP	GND

NOTE: WIRE TWISTED PAIR CABLE AS DENOTED BY ZIG-ZAG LINES



BENDIX PT1H-24-61 HERMETIC MALE (BACK VIEW)  
 PTO2A-24-61P PANEL MTG MALE (BACK VIEW)  
 PT06A-24-61S-SR CABLE MTG FEMALE (FRONT VIEW)

<b>UNIVERSITY OF CALIFORNIA LICK OBSERVATORY</b>		<b>61-PIN CONNECTOR SIGNALS SDSU CCD CONTROLLER</b>	
<b>REVISION</b>		DES'N BY: <b>B. Alcott</b>	ORIGIN DATE: <b>09-20-00</b>
09-20-00 MODIFIED FOR HI-RHO CCD CONTROLLER 09-26-00 ASSIGNED GROUNDS AND TWISTED PAIRINGS FOR 61-PIN CABLE 09-29-00 ADDED DEWAR ID LINES 06-15-01 CHANGE PIN u FROM Rg(AL) TO Vrg 06-19-01 CHANGED 'DC RESTORE' TO 'N+ GUARD CONTROL' 06-20-01 CHANGE SCHEMATIC NUMBER FROM EL-1286 TO EL-1290 CHANGED PIN C FROM VDD (AL) TO N+ SUB V(OLTAGE)		DRAWN BY:	MODIFY DATE: <b>07-10-01</b>
		PATH: HI_RHO_CCD/HI_RHO_CONN61	
		DWG. NO.	NUM. <b>1</b> OF <b>1</b>
		<b>EL-1290</b>	