

41 PIN ELECTRONICS BOX CONNECTOR

PIN	SIGNAL	PIN	SIGNAL
A	- (RESERVED FOR SHIELDS)	a	V2
B	GND (Vrg)	b	SUMMING WELL
C	GND (H3AL)	c	GND (V2)
D	Vrg	d	GND (WELL)
E	H3AL	e	N+ SUB VOLTAGE
F	V3	f	H1
G	GND (V3)	g	Vsub
H	ID 0	h	GND (Vdd)
J	ID 1	i	GND (Vsub)
K	ID 2	j	GND (H1)
L	GND (Vr)	k	GND (V1)
M	GND (TG)	m	GND (H3AR)
N	Vr	n	V1
P	TG	p	H3AR
R	Vopg	q	H2AR
S	N+ GUARD CONTROL	r	-
T	GND (Vopg)	s	GND (H2AR)
U	GND (N+ GUARD CTL)	t	-
V	ID 3		
W	SPARE		
X	ID 4		
Y	GND (H2AL)		
Z	H2AL		

REVISION

09-26-00	REMOVED POWER LINES, HEATER, AND TEMPERATURE DIODE SIGNALS
09-29-00	ADDED DEWAR ID LINES
06-07-01	UPDATED PIN ASSIGNMENTS TO MATCH THOSE ON EL-1283; RENAMED DRAWING TO DIFFERENTIATE 41-PIN ELECTRONICS BOX CONNECTOR FROM 41-PIN HERMETIC CONNECTOR
06-20-01	CHANGE SCHEMATIC NUMBER FROM EL-1284 TO EL-1279
07-09-01	ADDED N+ SUB VOLTAGE SIGNAL (replaces Vdd)

**UNIVERSITY OF CALIFORNIA
LICK OBSERVATORY**

**41 PIN ELECTRONICS BOX
CONNECTOR
HI RHO CCD CONTROLLER**

DES'N BY: **B. Alcott**

ORIGIN DATE: **02-27-00**

DWG. NO.

NUM. 1 OF 1

Last SN:

MODIFY DATE: 07-09-01

PATH: HI_RHO_CCD\HI_RHO_41PINEBOX.SCH

REV. A

EL-1279